

FIG. 1

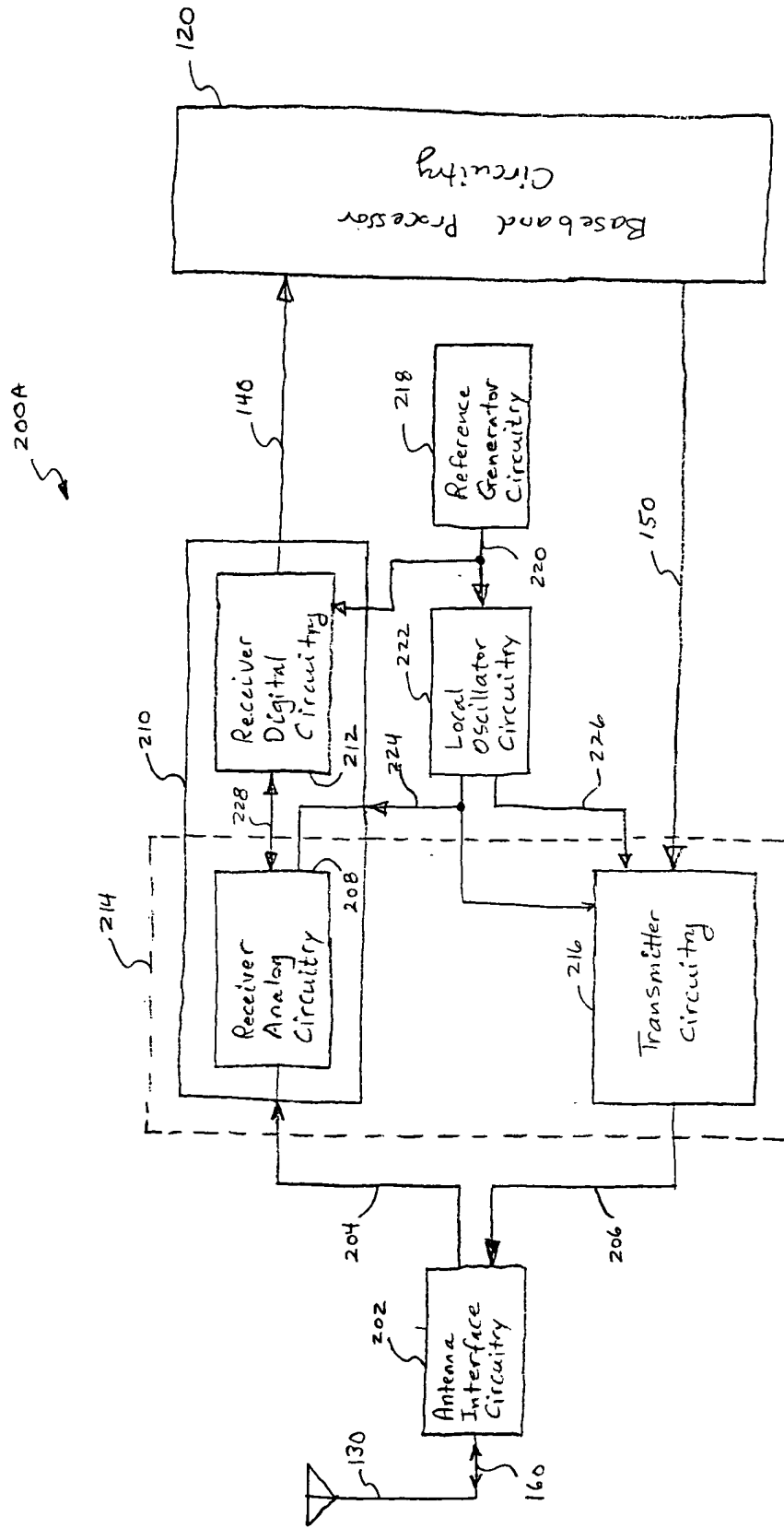


FIG. 2A

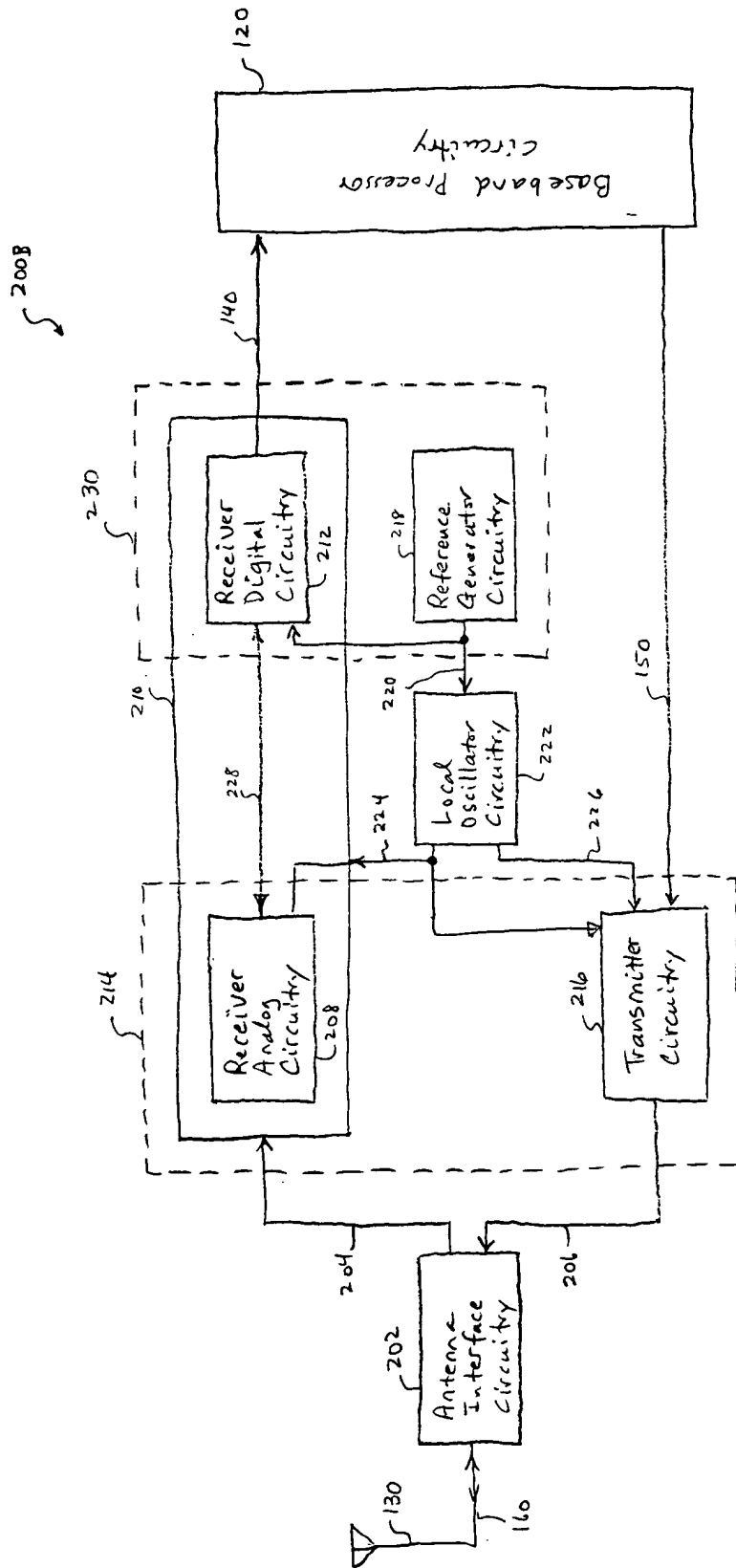


FIG. 2B

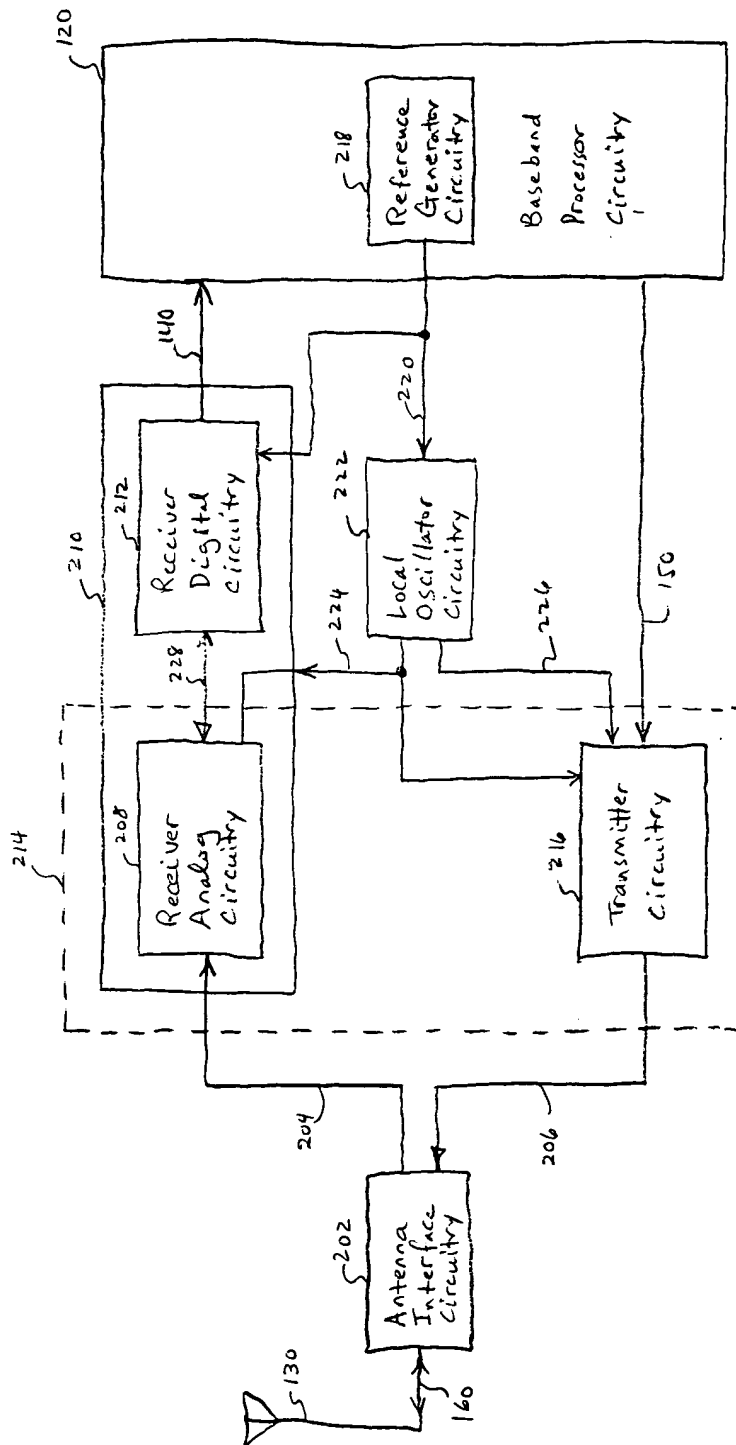


FIG. 2C

FIG. 2D

200D

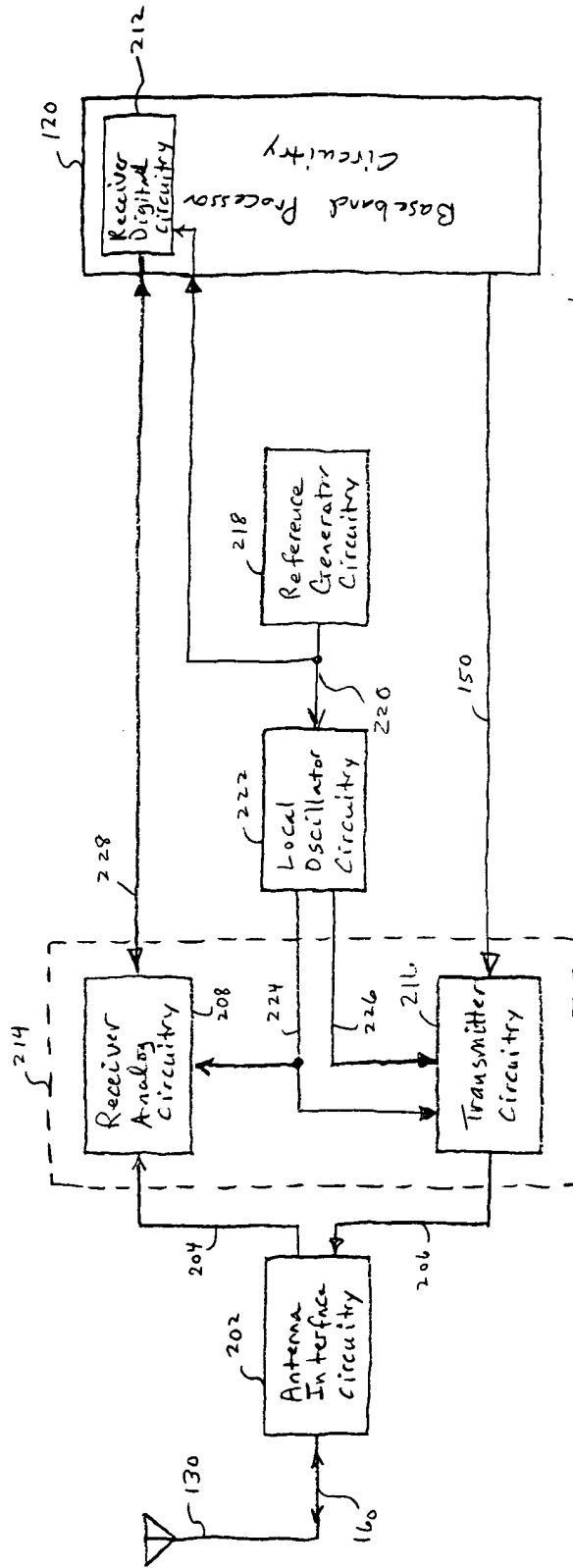


FIG. 2D

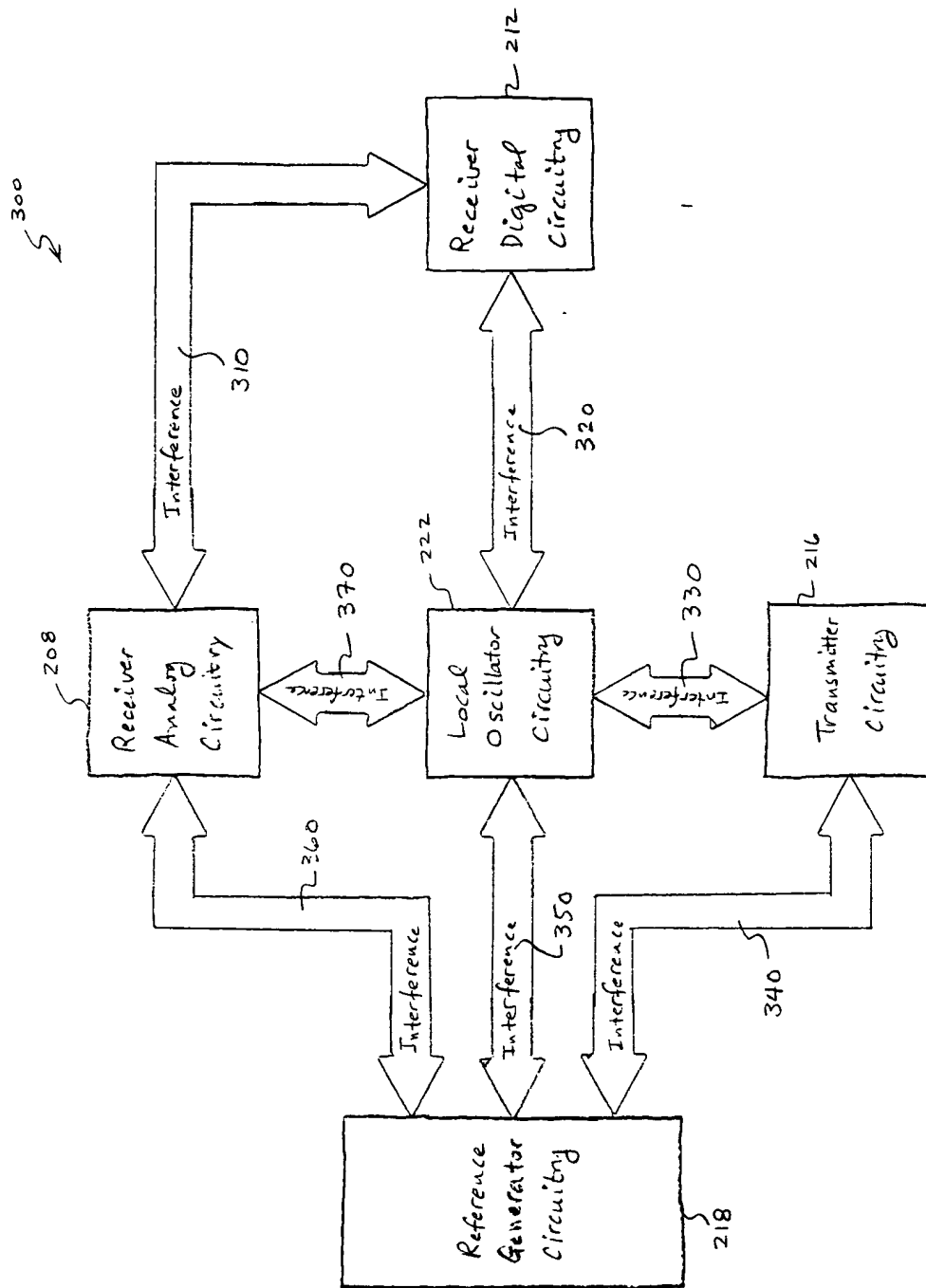


FIG. 3

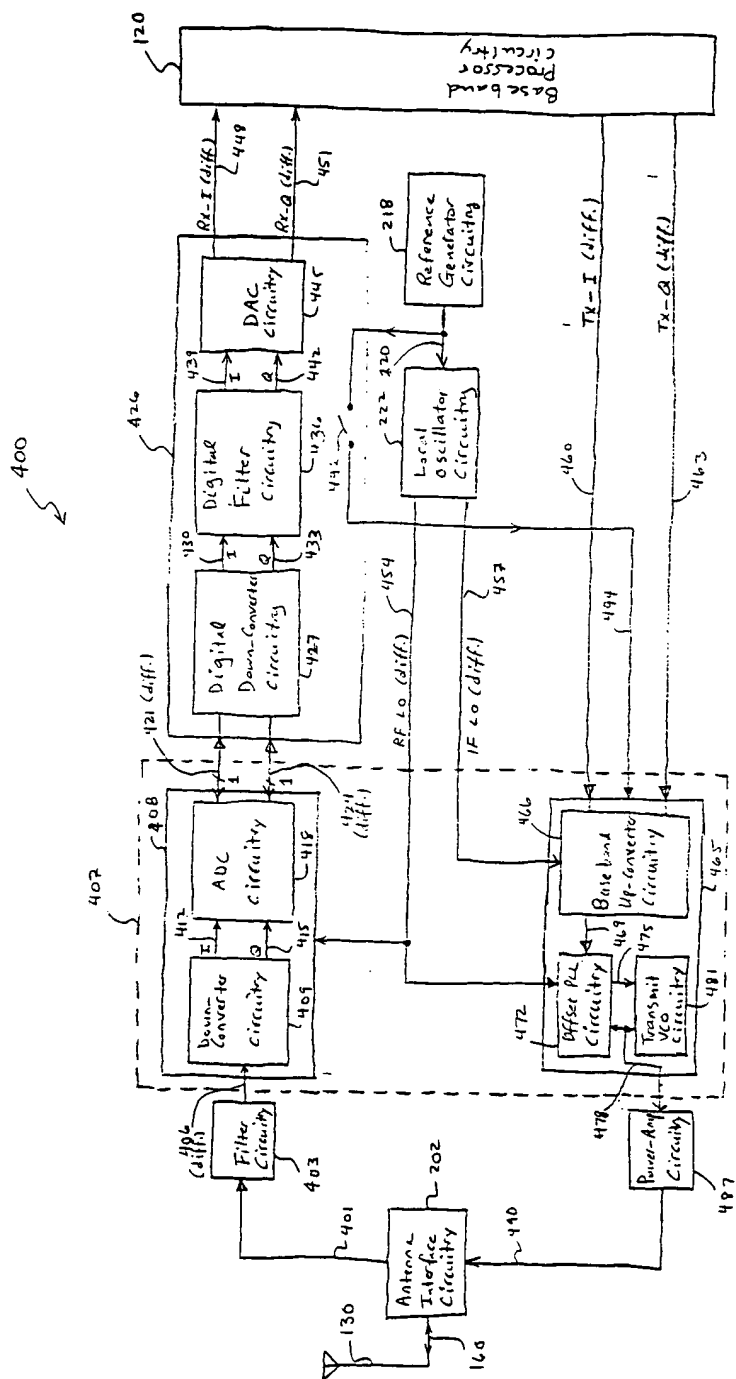


FIG. 4

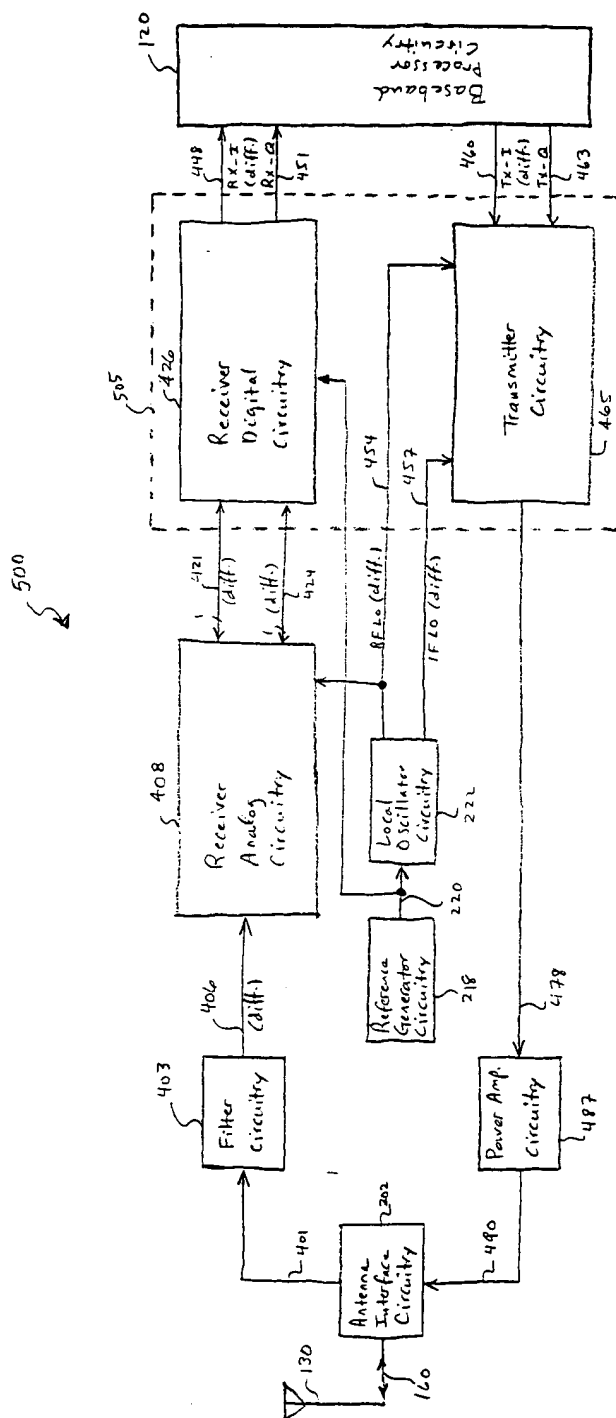


FIG. 5



FIG. 6 is a block diagram of a radio frequency (RF) transceiver system 600. The system 600 includes an antenna 130, an antenna interface circuitry 202, a filter circuitry 406, a down-converter circuitry 409, a baseband processor 120, a power amplifier circuitry 478, and a transmitter circuitry 465. The system 600 is configured to receive and transmit radio frequency signals.

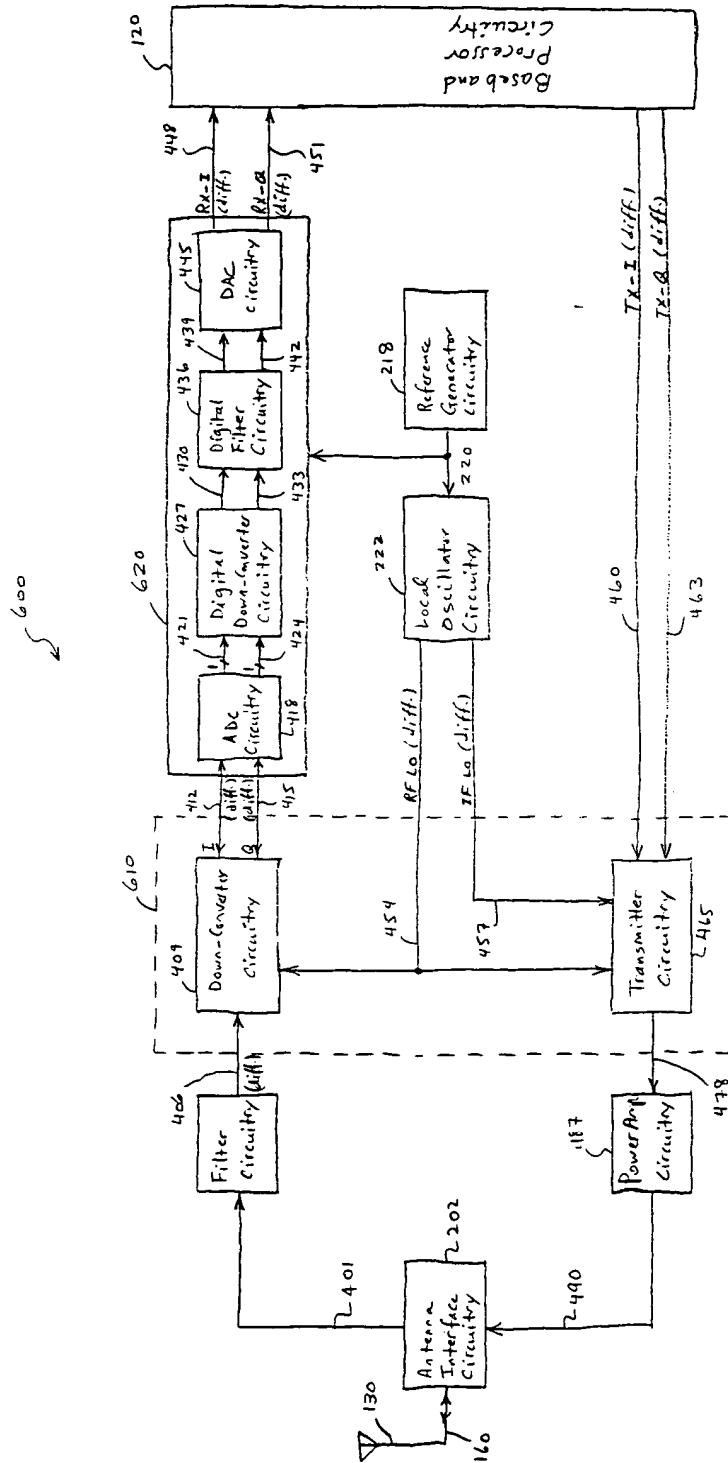


FIG. 6

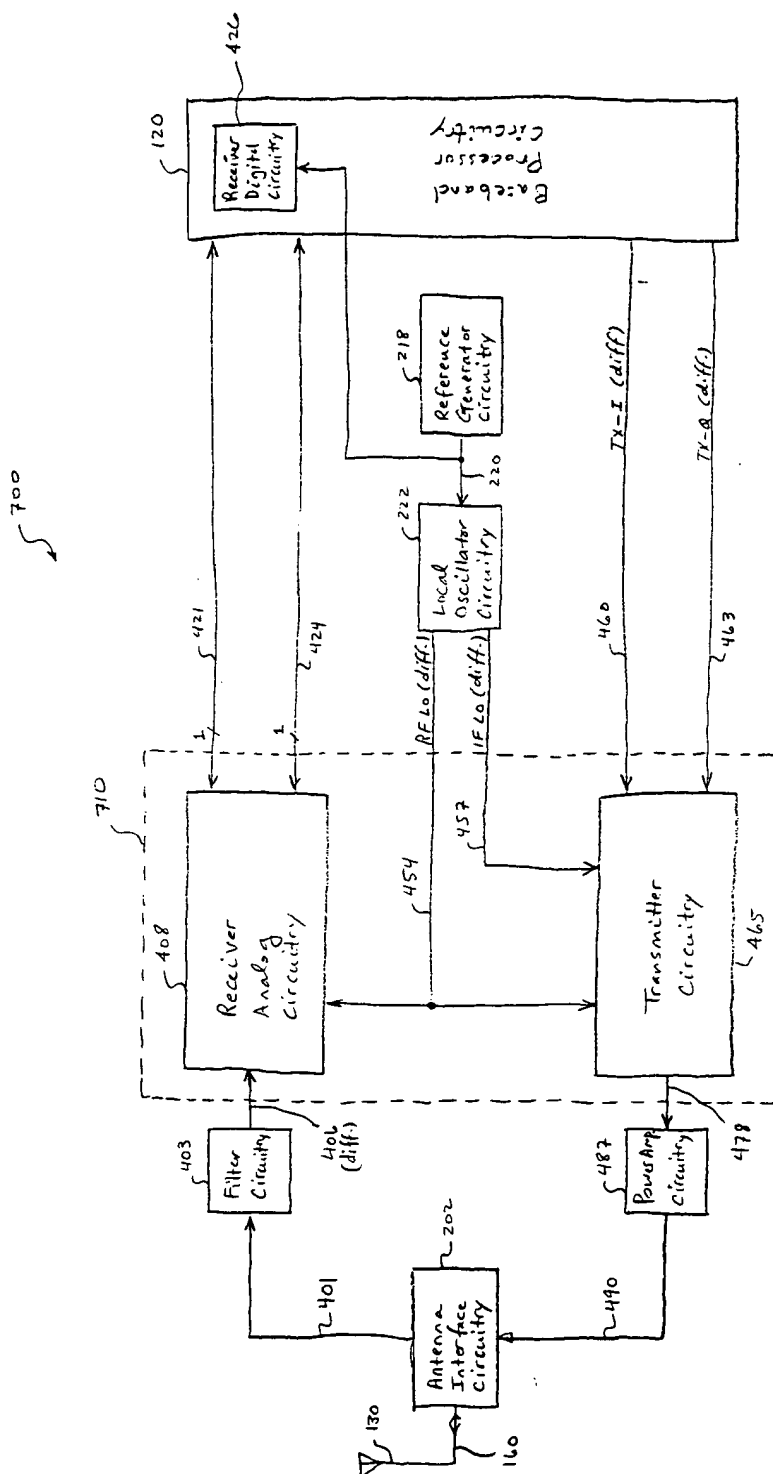


FIG. 7

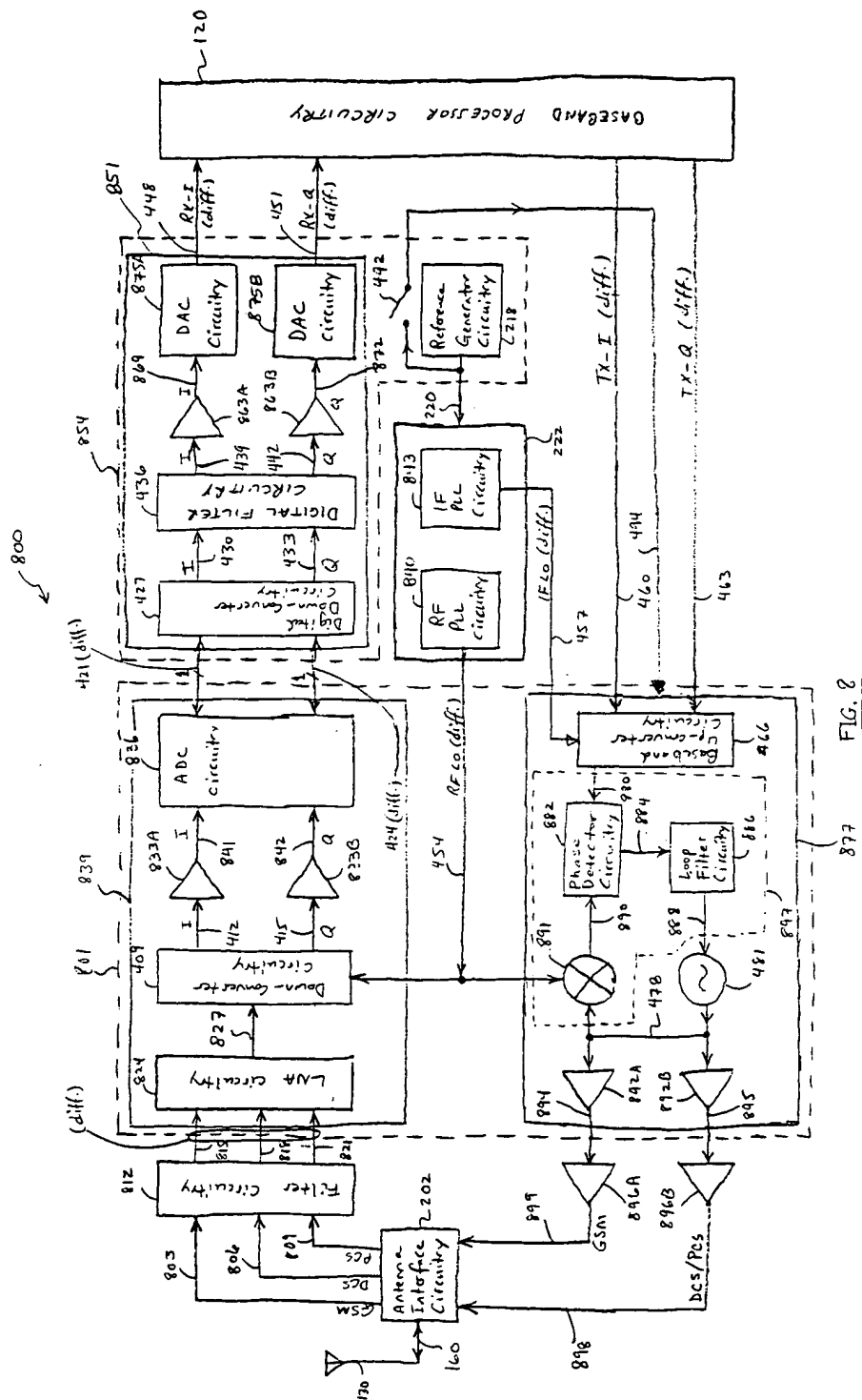


FIG. 2

910A

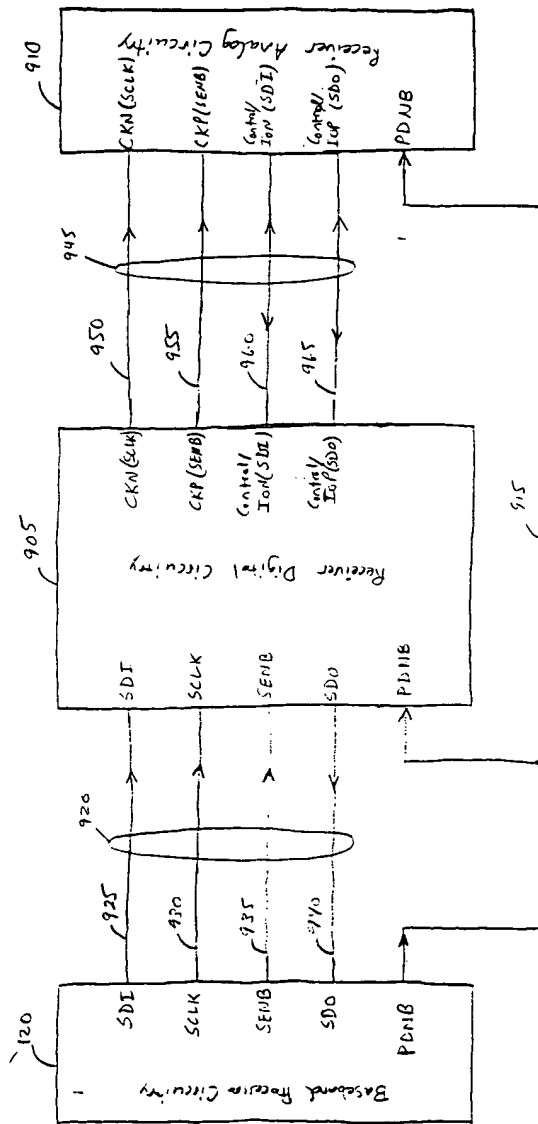


FIG. 9A

900B

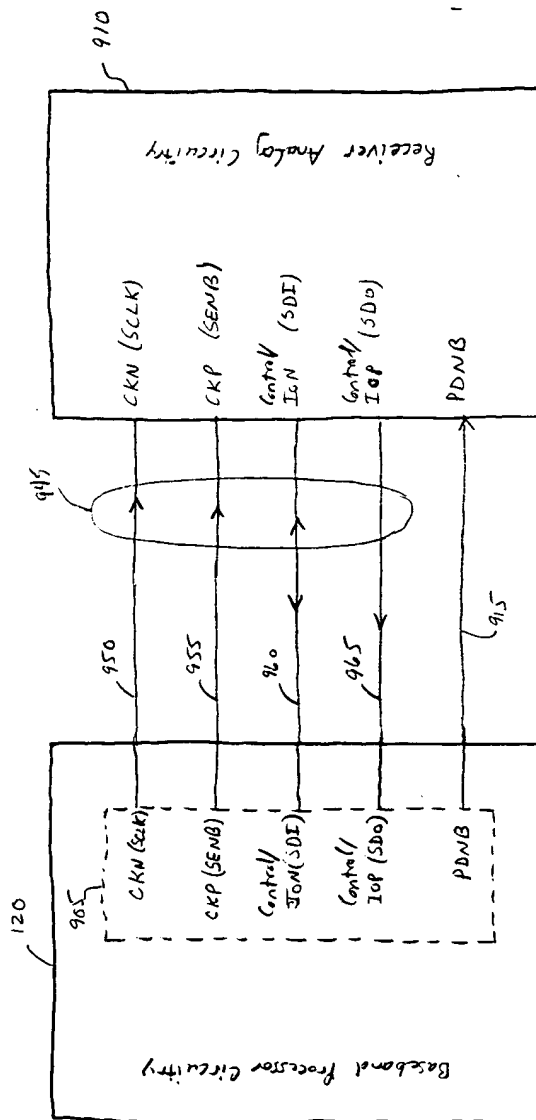


FIG. 9B

FIG. 10 is a block diagram of a receiver circuitry 1000, which includes a Baseband Receiver Circuitry 120, a Receiver Digital Circuitry 905, and a Receiver Analog Circuitry 910.

1000

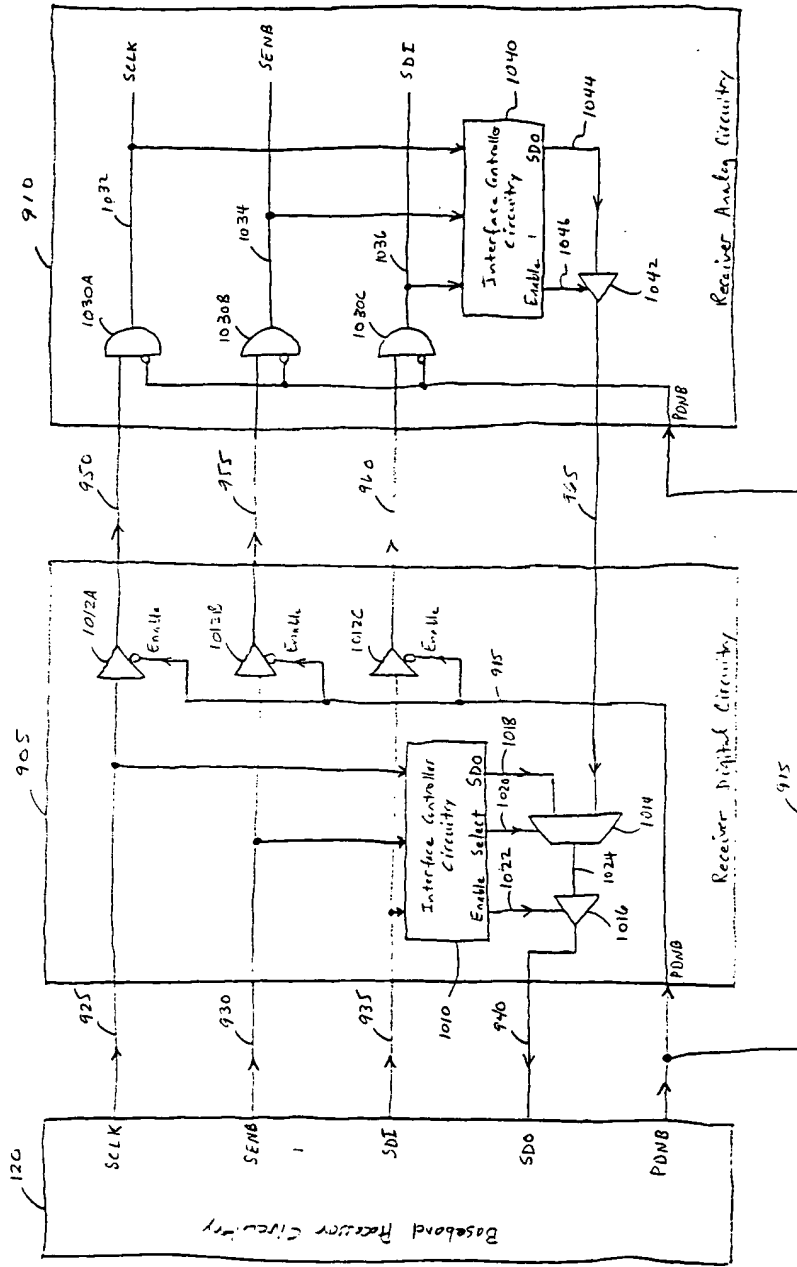


FIG. 10

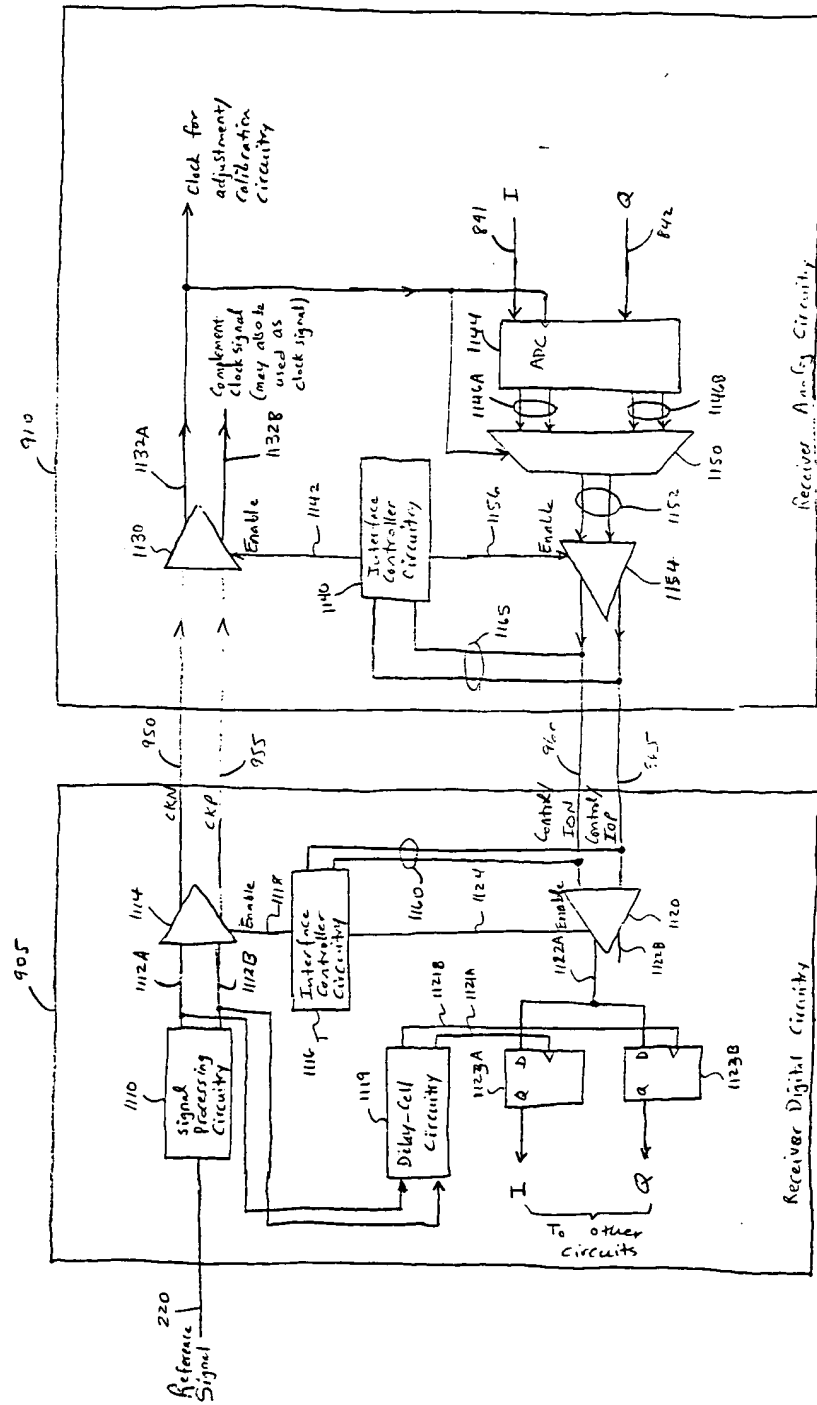


FIG. 11A

11008

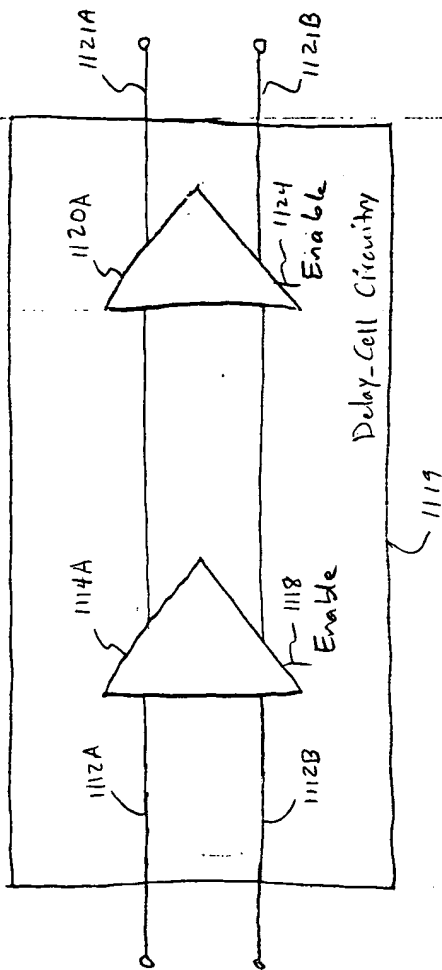


FIG. 11B



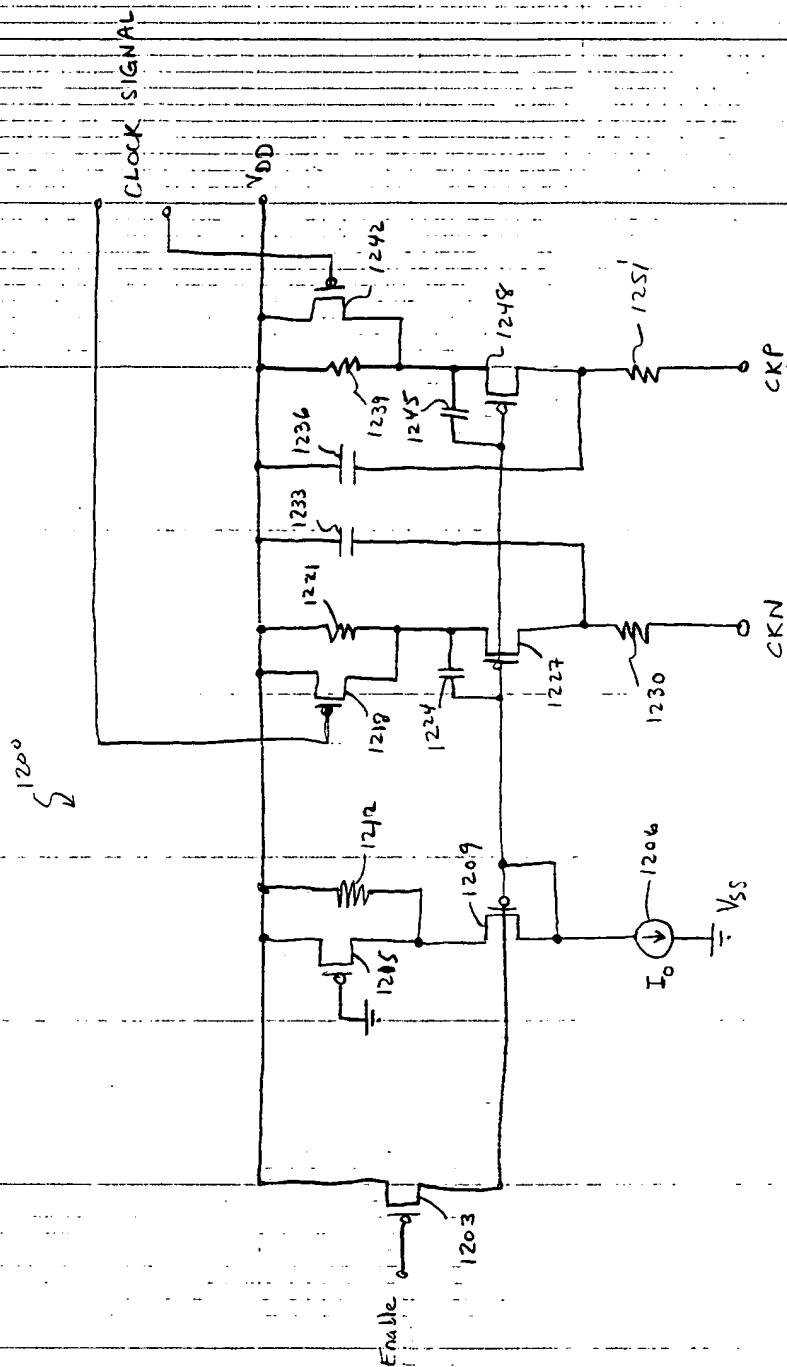
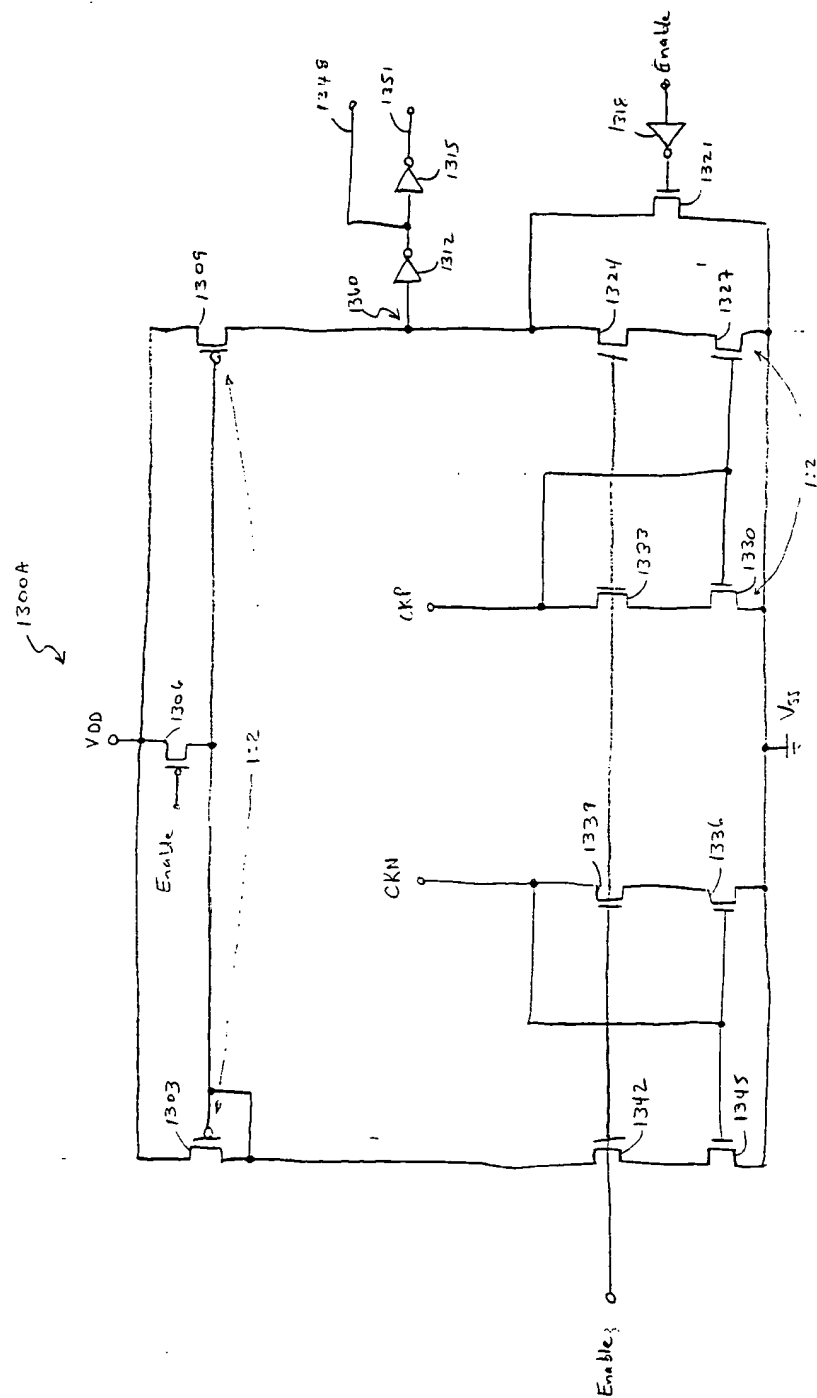


FIG. 12



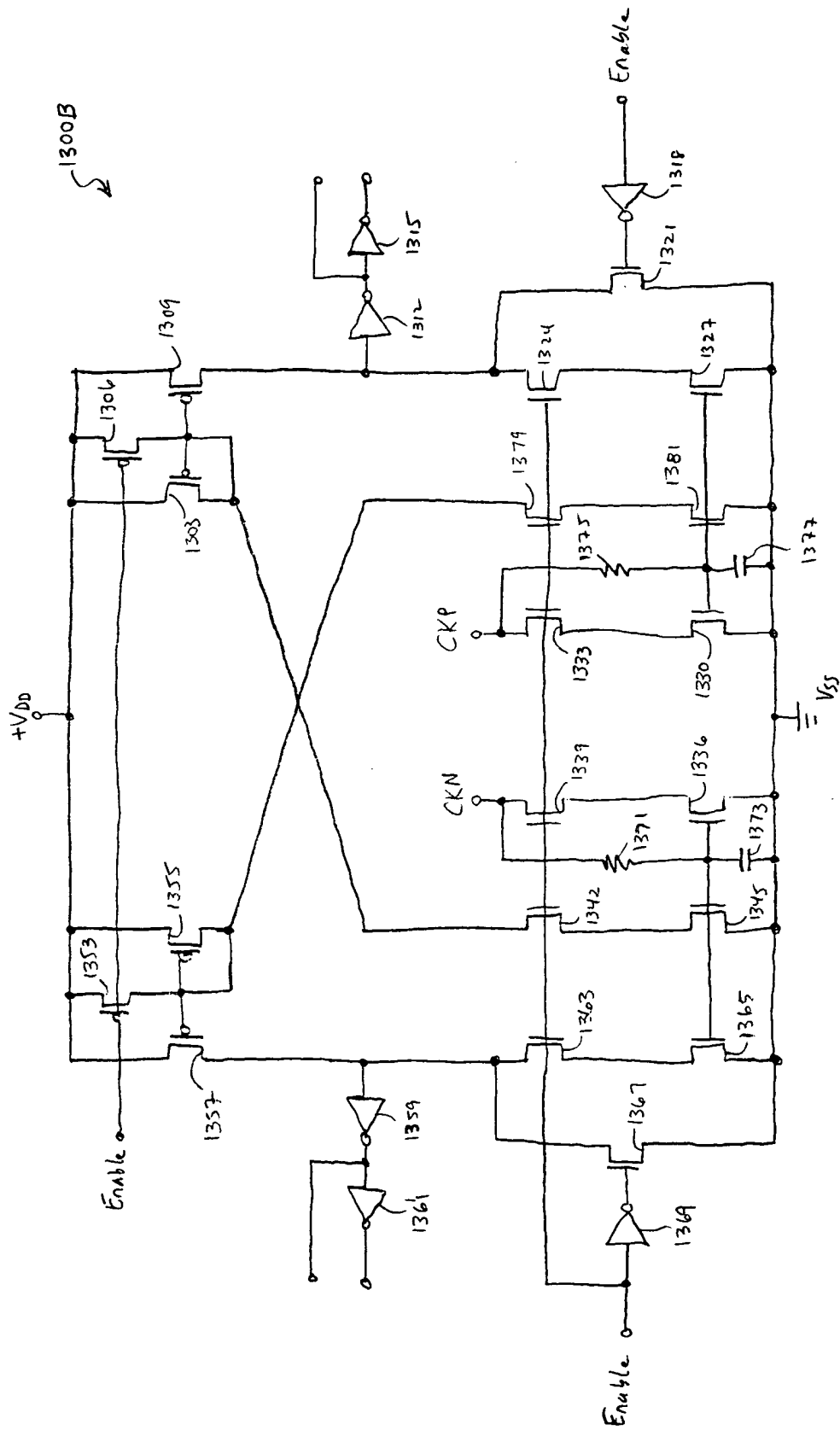


FIG. 13B

1400

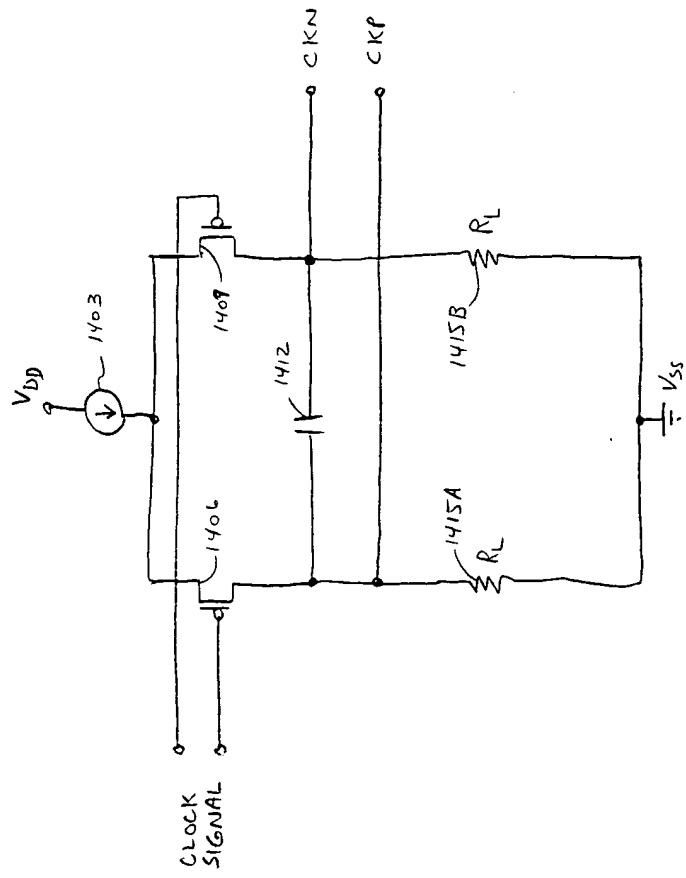


FIG. 14

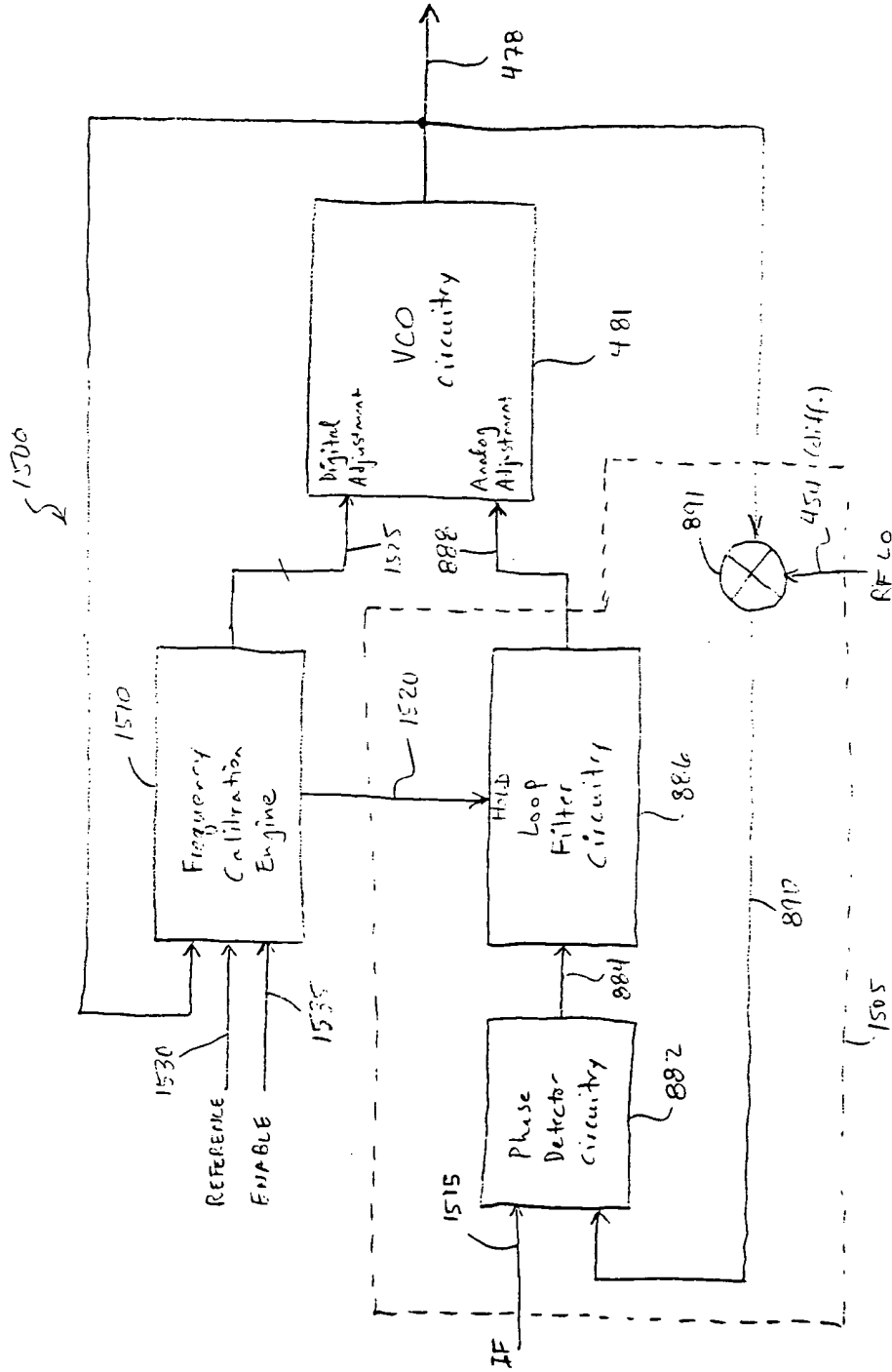


FIG. 15

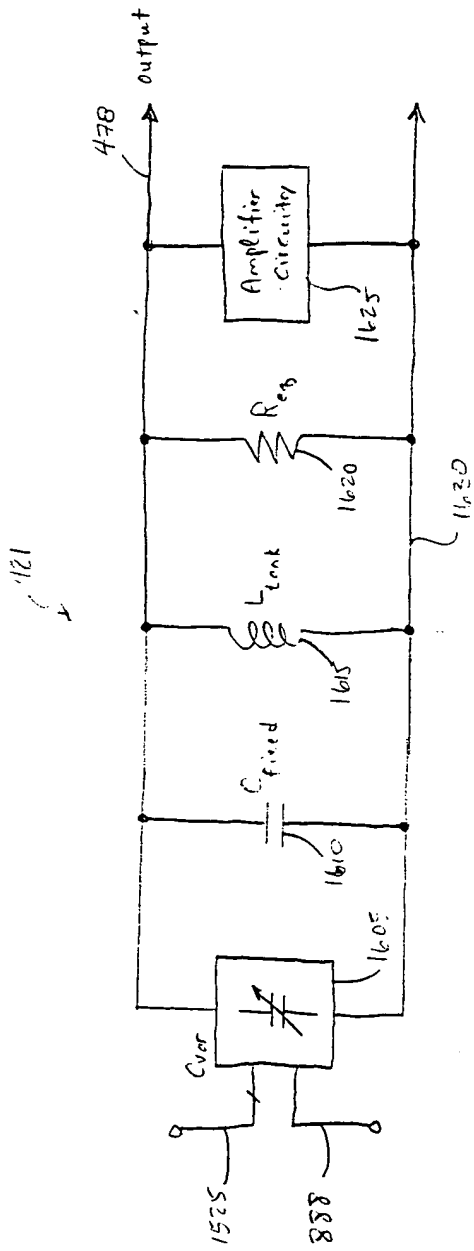


FIG. 16

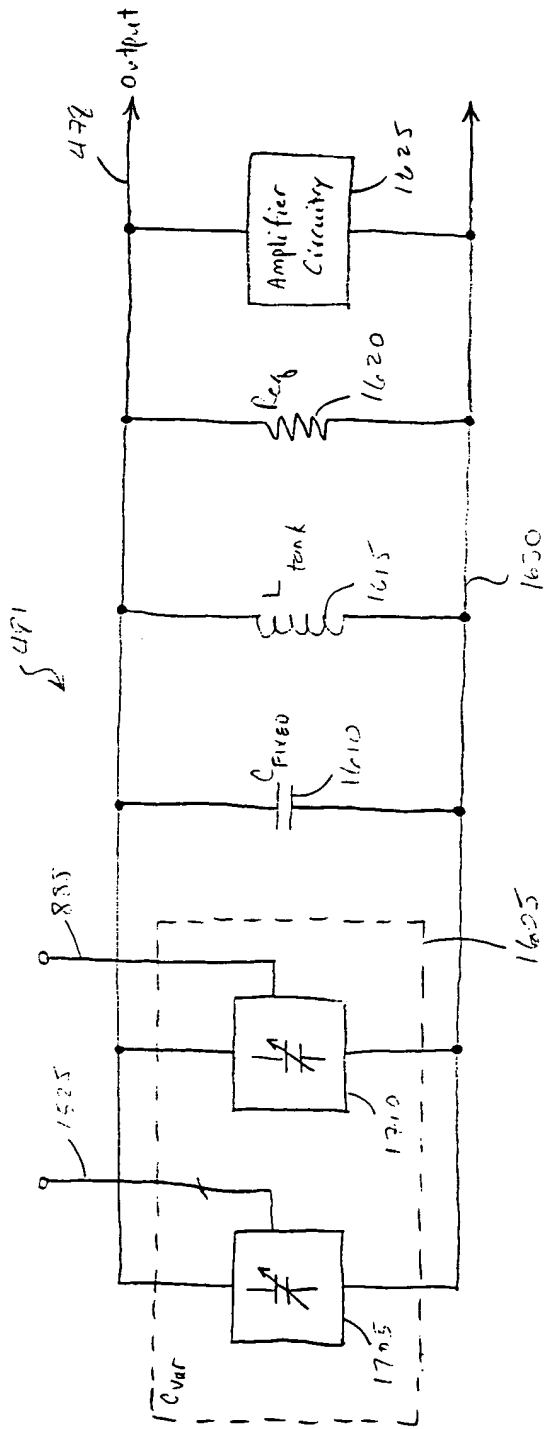


FIG. 17

FIG. 18

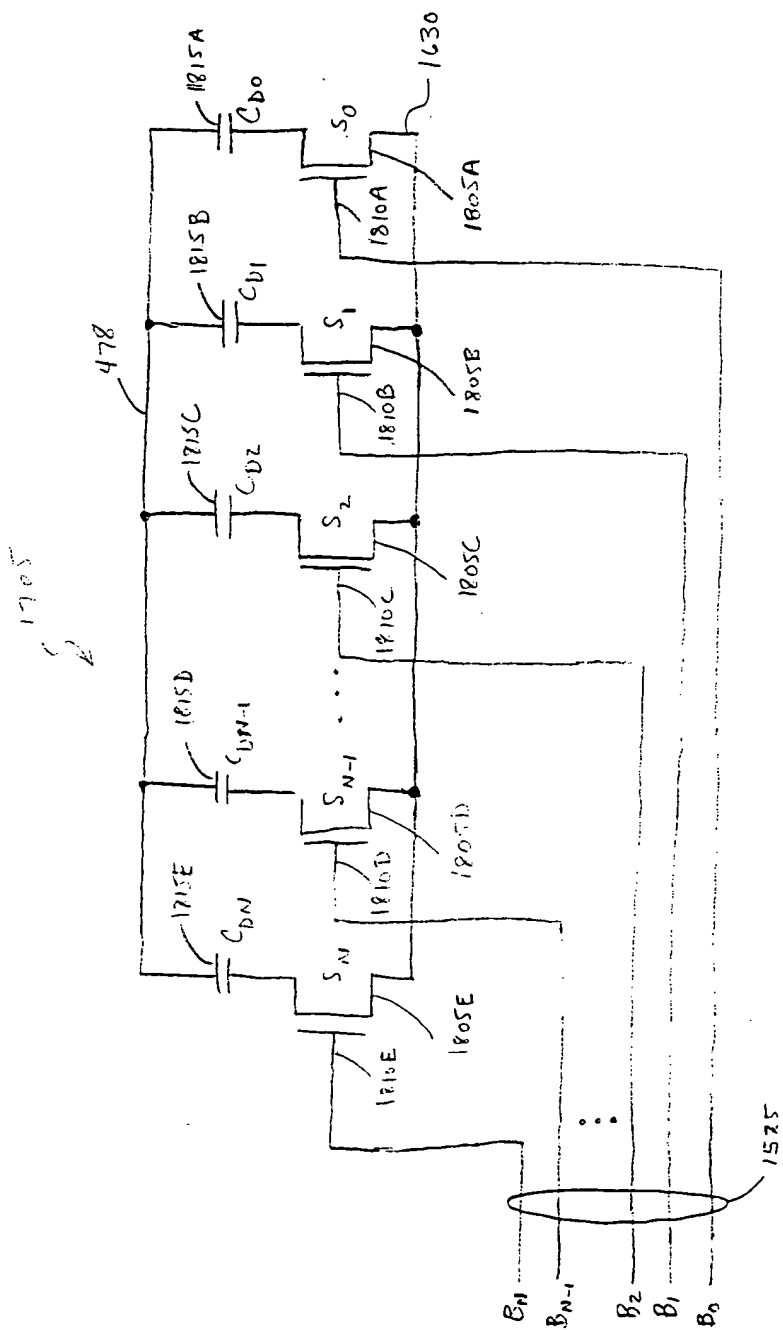


FIG. 18



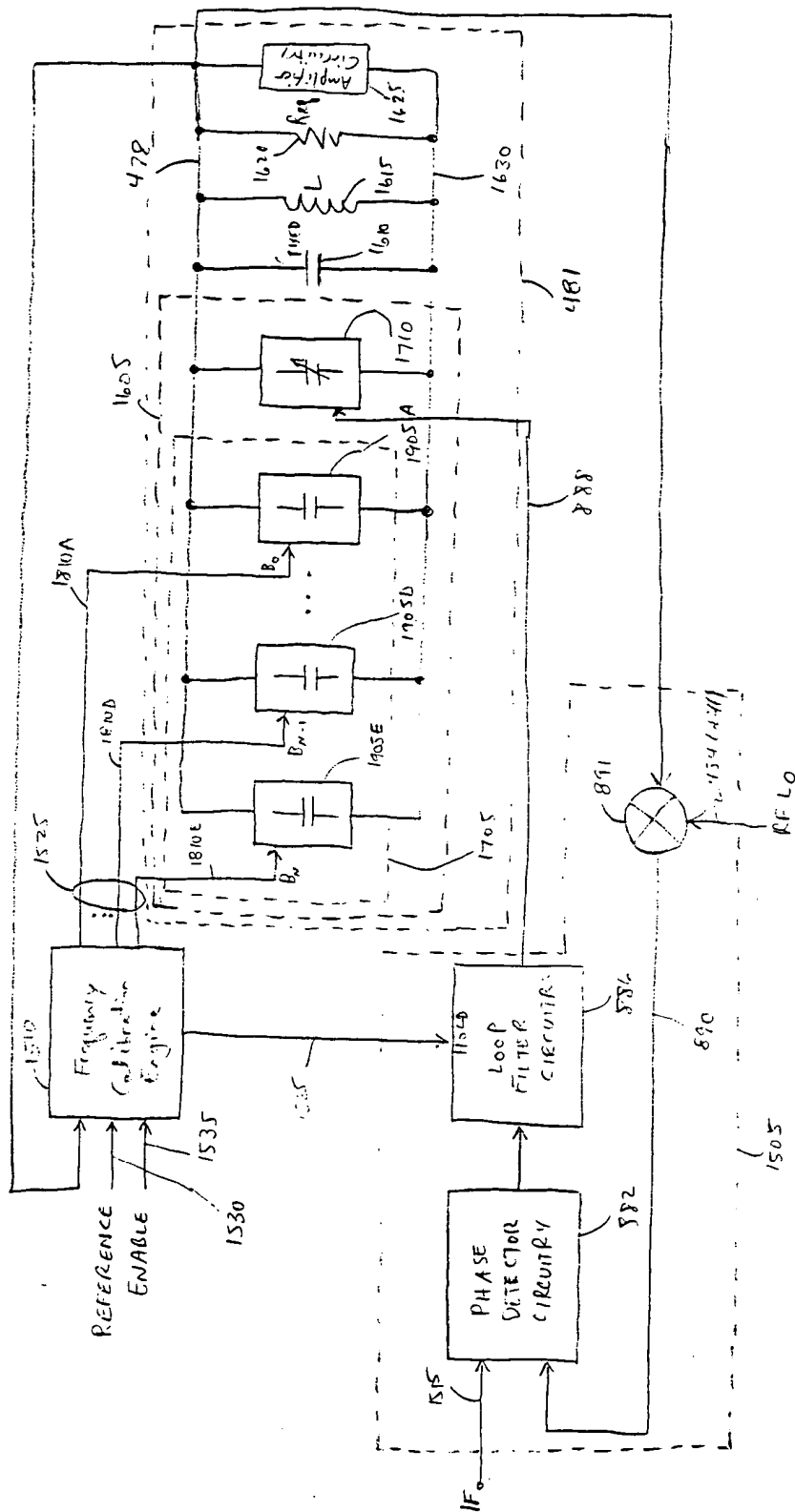
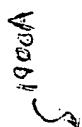


Fig. 19f



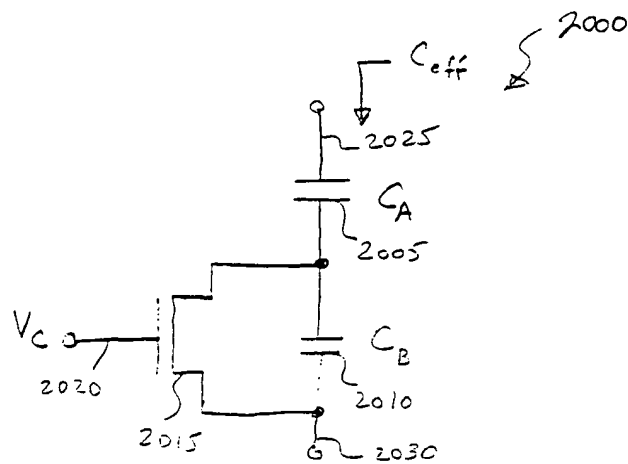


FIG. 20

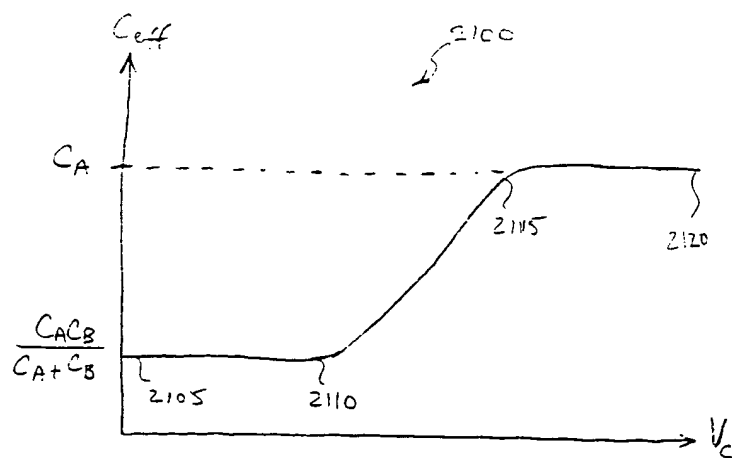


FIG. 21

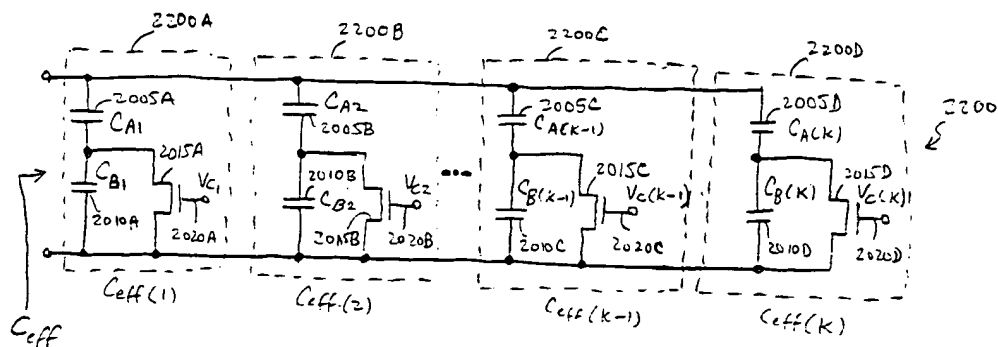


FIG. 22

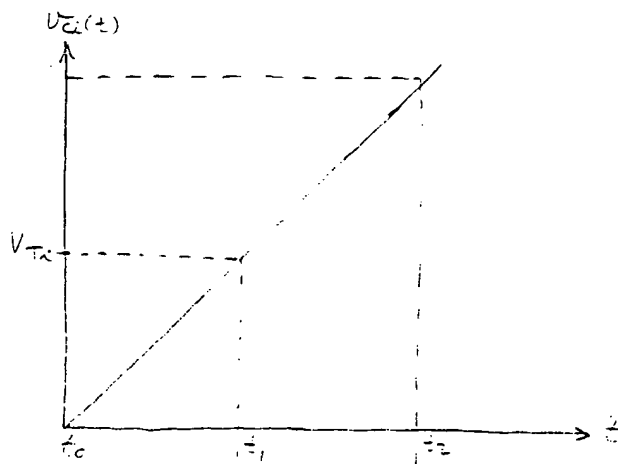


FIG. 23A

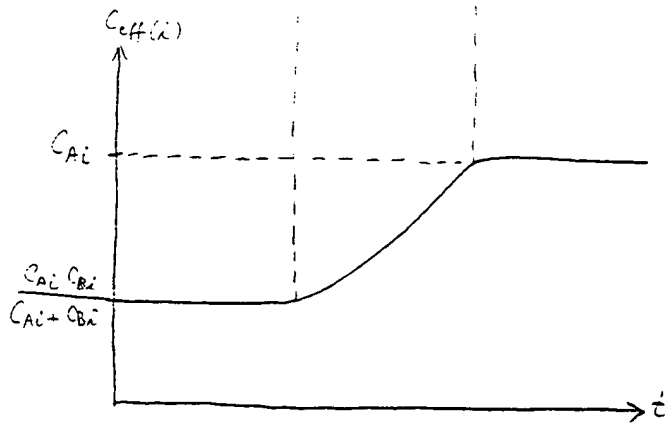


FIG. 23B

FIG. 24A

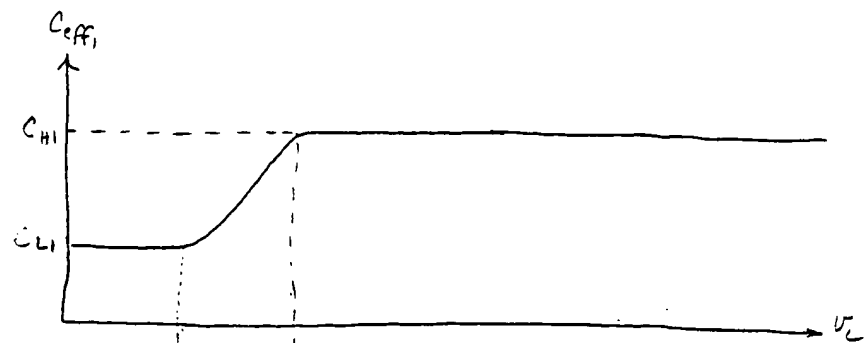


FIG. 24B

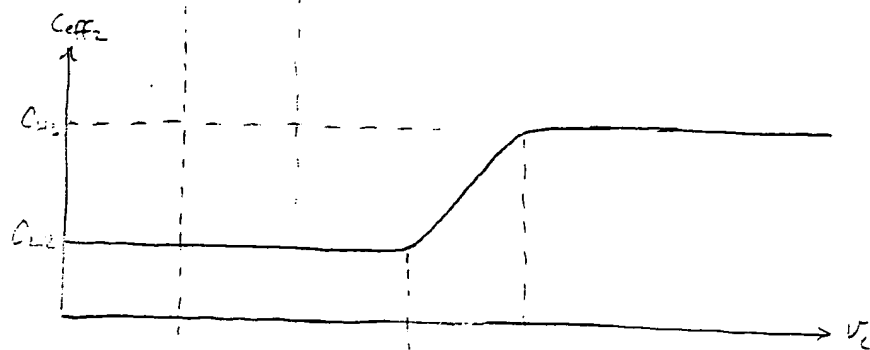


FIG. 24C

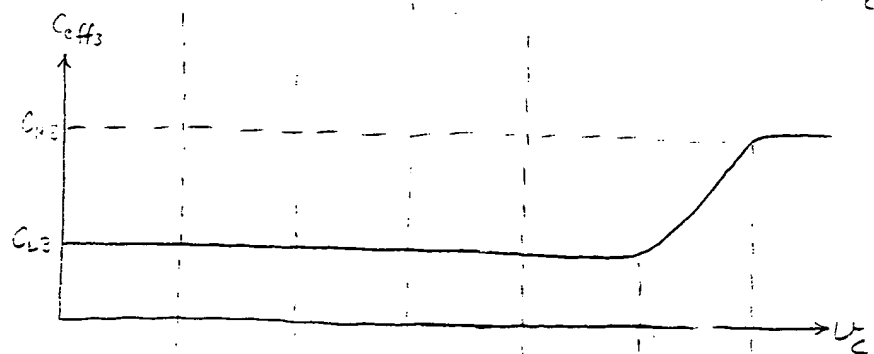
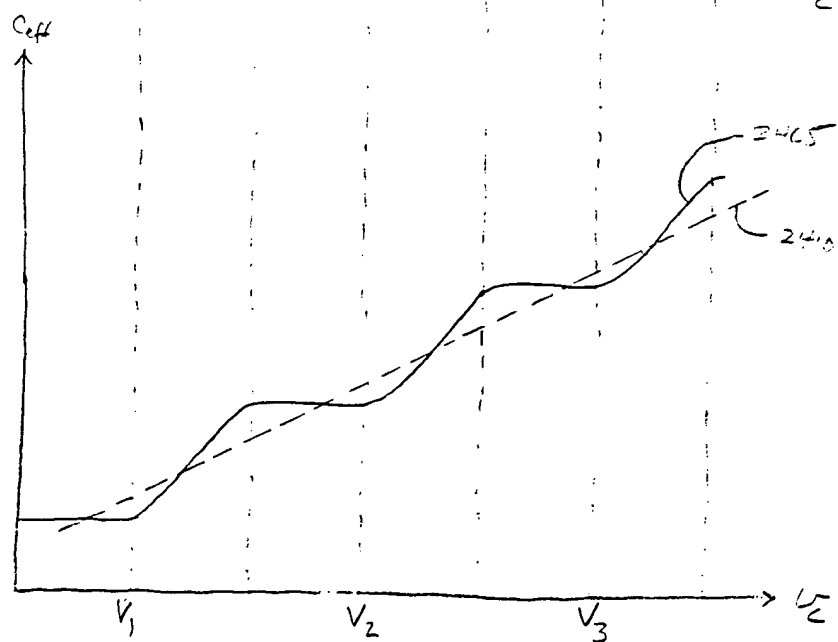


FIG. 24D



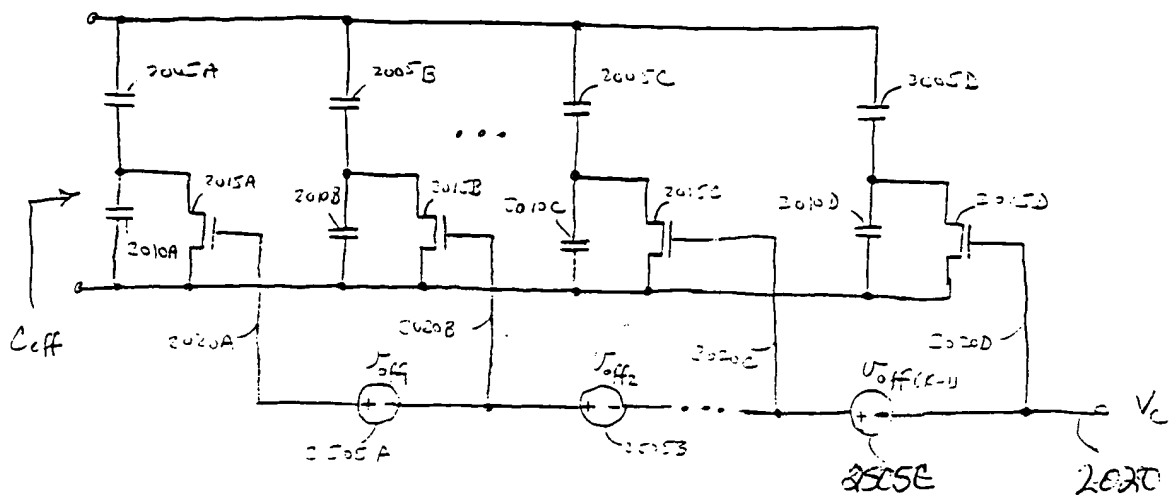


FIG. 25

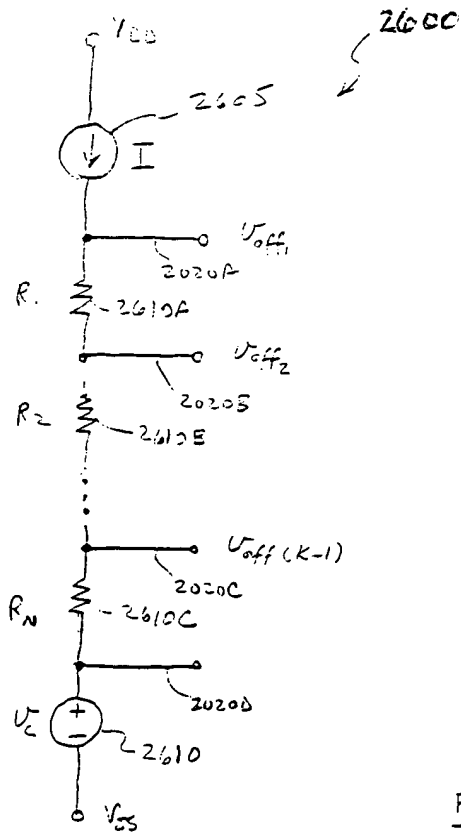


FIG. 26

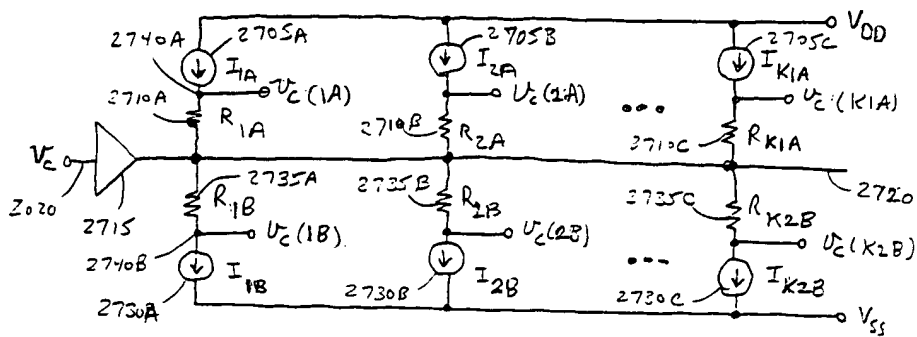


FIG. 27

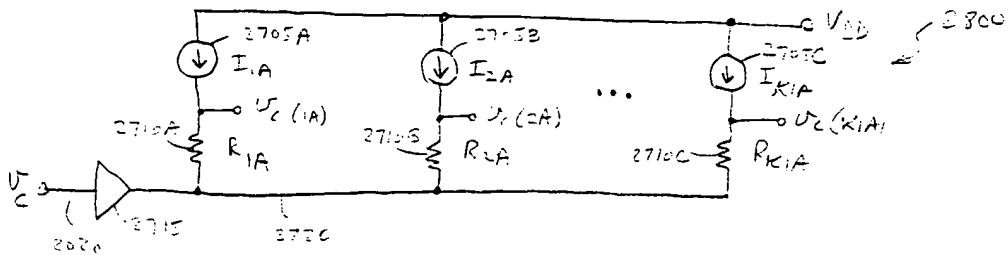


FIG. 28

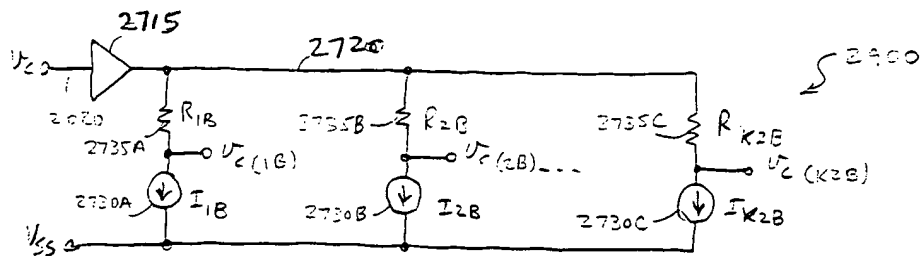


FIG. 29

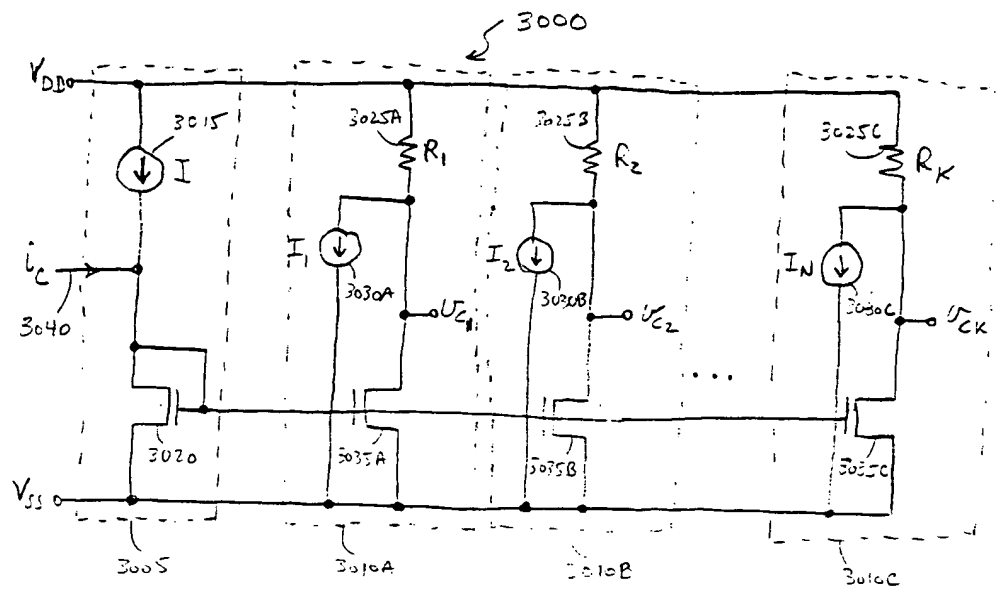


FIG. 30



3100A

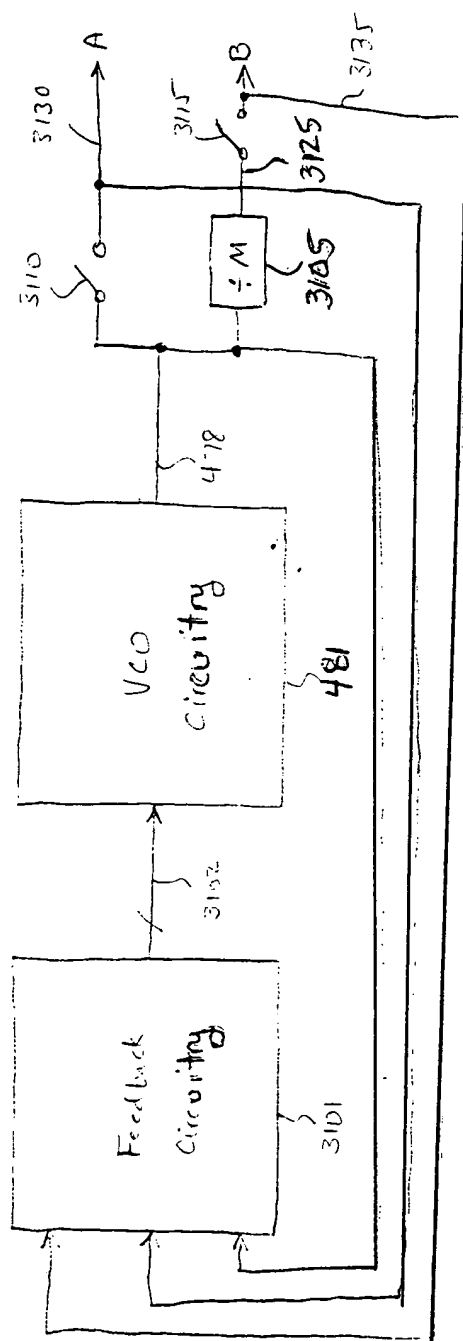
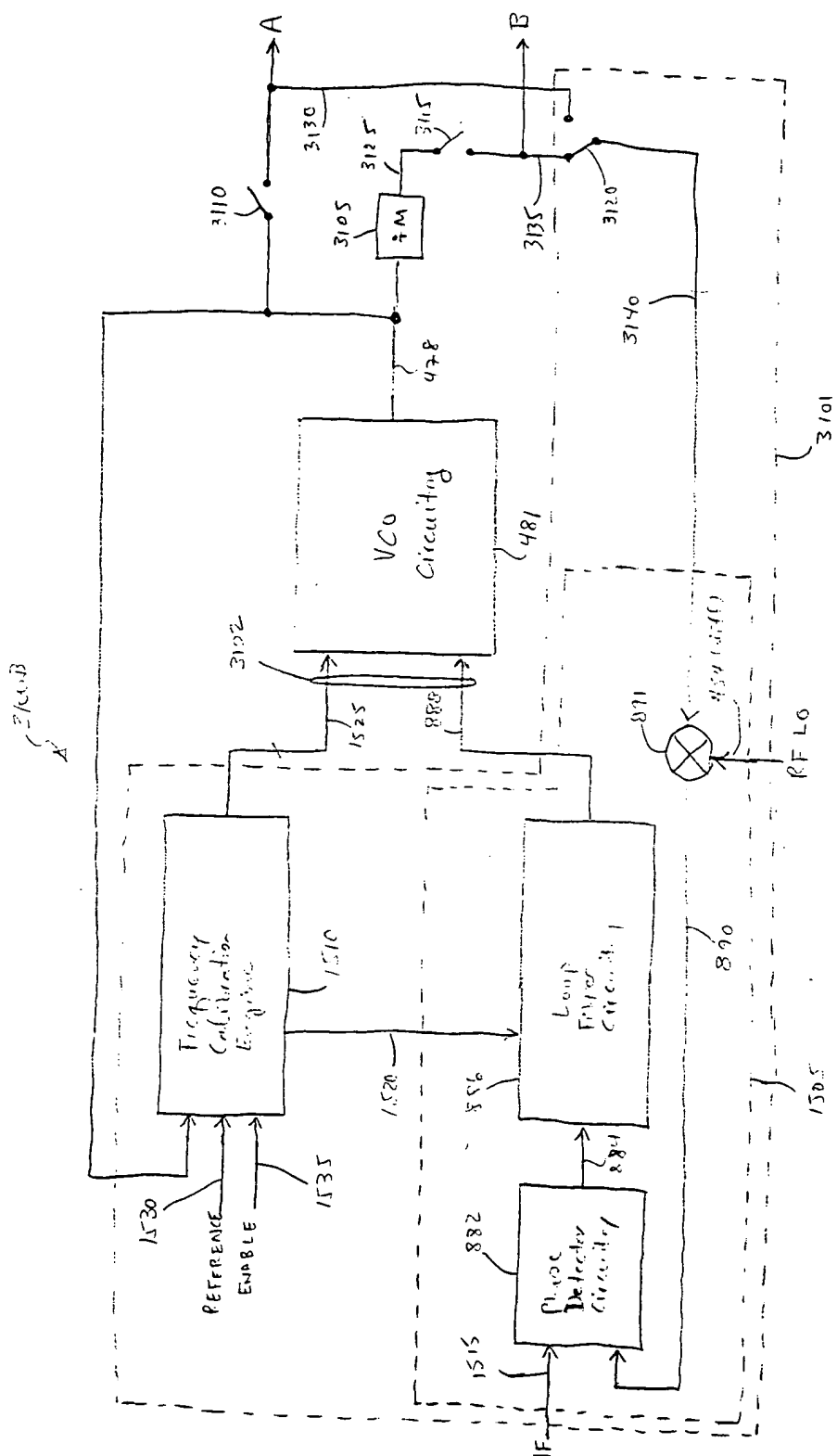


FIG. 31A



813.211



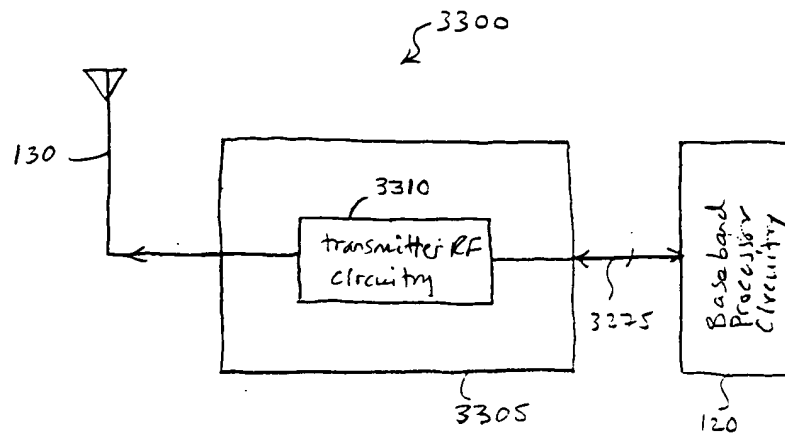


FIG. 33

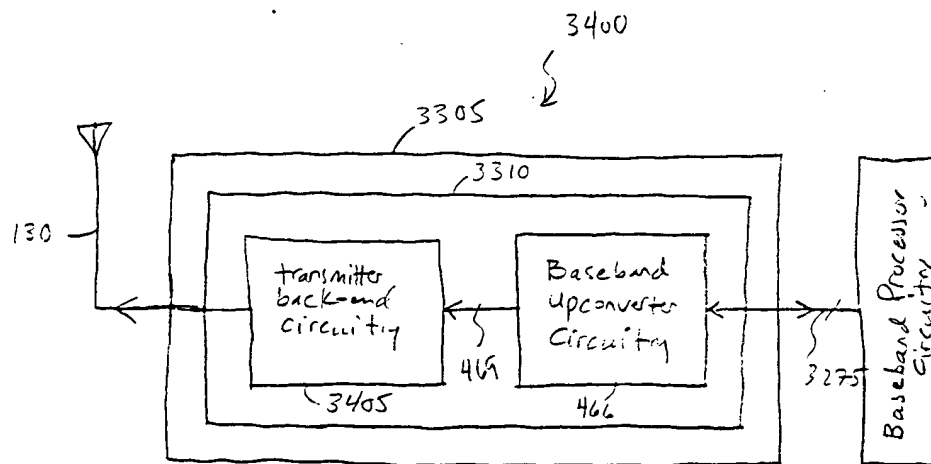


FIG. 34

